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## APPLICANTS

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## \*\* CONTINUING DATA \*\*\*\*\*

*OC Nme*

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN P2003-122256 04/25/2003

*OC*

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

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Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance <i>David O. Day</i> Examiner's Signature Initials	STATE OR COUNTRY JAPAN	SHEETS DRAWING 5	TOTAL CLAIMS 11	INDEPENDENT CLAIMS 1
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## TITLE

Semiconductor integrated circuit having system bus divided in stages

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